

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Jacek Budny et al.  
Serial No. : 10/723,454  
Filed : November 26, 2003  
Title : CO-PROCESSING

Art Unit : 2143  
Examiner : Unknown

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450


INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449.

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 26 April 2004

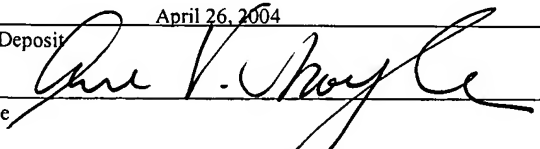
  
Anthony T. Moosey  
Reg. No. 55,773

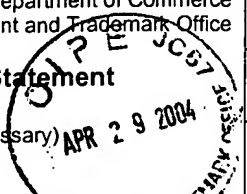
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Substitute Form PTO-1449 (Modified)  <b>Information Disclosure Statement</b> <b>by Applicant</b> (Use several sheets if necessary)  (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office  	Attorney's Docket No. 10559-874001	Application No. 10/723,454
		Applicant Jacek Budny et al.	
		Filing Date November 26, 2003	Group Art Unit 2143

### U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

### Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

### Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AQ	Intel Corporation - Intel®IXP2400 Network Processor – 2 <sup>nd</sup> General Intel®NPU
	AR	M. Adiletta et al., The Next Generation of Intel IXP Network Processors, 8/15/02, Vol. 6, pp. 6-19
	AS	<a href="http://www.electronicstalk.com/news/cpy/cpy160.html">www.electronicstalk.com/news/cpy/cpy160.html</a> . ADR interface gains acceptance
	AT	<a href="http://www.altera.com/products/devices/apex/features/apx-dram.html">www.altera.com/products/devices/apex/features/apx-dram.html</a> , APEX Device Family: External Memory
	AU	<a href="http://www.embeddedstar.com/press/content/2002/7/embedded4318.html">www.embeddedstar.com/press/content/2002/7/embedded4318.html</a> , NPF LA-1 Interface Specification Compatible with QDR SRAM

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-874001	Application No. 10/723,454
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)  (37 CFR §1.98(b))		Applicant Jacek Budny et al.	
		Filing Date November 26, 2003	Group Art Unit 2143

<b>Other Documents (include Author, Title, Date, and Place of Publication)</b>		
Examiner Initial	Desig. ID	Document
	AV	Intel®IXP2400 Network Processor/Vitesse GigaStream® Switch Fabric Solution, 11/02

Examiner Signature	Date Considered
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